REMARKS

Claims 1-5, 16, 17 and 37-49 have been canceled and claims 50-60 have been added. Claims 5-15 and 50-60 are pending in the application. Action on the merits is requested in view of the amendments and the remarks to follow.

The Examiner has required restriction between the following alleged species:

Species 1: claims 1-4, allegedly drawn to a processing method for IC including the formation of memory and peripheral circuitry;

Species 2: claims 5-15, allegedly drawn to a processing method masking with a common mask to form 3 different threshold voltages;

Species 3: claims 16 and 17, allegedly drawn to a processing method masking with a common mask to form memory access devices;

Species 4: claims 37-40, allegedly drawn to a processing method forming a plurality of NMOS devices;

Species 5: claims 41-45, allegedly drawn to a method to improve DRAM storage cell retention time;

Species 6: claims 41-45 (Applicant assumes that the Examiner had intended claims 46-49), allegedly drawn to a method to improve DRAM storage cell retention time having different implants and junction structures. The Examiner also alleges that there is no generic claim.

Applicant elects claims 5-15 without traverse. Accordingly, Applicant cancels claims 1-4, 16, 17 and 37-49 without prejudice. Applicant also adds new claims 50-60.

New claims 50-60 are similar to claims 5-15 but differ in scope. New claims 50-60 are supported at least by text appearing at p. 5, line 5 through p. 12, line 10 of the application as originally filed. No new matter is added by new claims 50-60. New claims 50-60 distinguish over the art of record and are allowable.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page(s) are captioned "Version with markings to show changes made".

This application is believed to be in condition for allowance and action to that end is requested. The Examiner is requested to telephone the undersigned in the event that the next office action is one other than a Notice of Allowance. The undersigned is available during normal business hours (Pacific Time Zone).

Respectfully submitted,

Dated: 0 4. 3, 2002 By:

Frederick M. Fliegel, Ph.D.

Reg. No. 36,138



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/848,846
Filing Date	May 3, 2001
Inventor	Luan C. Tran
Assignee	Micron Technology, Inc.
Group Art Unit	2813
Examiner	L. Schillinger
Attorney's Docket No	MI22-1689
Title: Semiconductor Processing Methods of Forming Integrated Circuitry	

y's Docket No. emiconductor Processing IVIC 37 CFR §1.121(b)(1)(iii) AND 37 CFR §1.121(c)(1)(ii) FILING REQUIREMENTS TO ACCOMPANY RESPONSE TO SEPTEMBER 16, 2002 OFFICE ACTION REPTEMBER 16, 2002 OFFICE ACTION

In the Claims

Claims 1-4, 16, 17 and 37-49 have been canceled without prejudice Claims 50-60 have been added.

Because these amendments merely cancel some claims and add other claims, no amendments involving changes to the application or specification are being made. Accordingly, there are no marked-up versions of claims or specification required or provided.

END OF DOCUMENT